**IT3280E Computer Architecture Lab**

**Nguyễn Đức Anh**

**MSSV: 20235890**

**Lab 4: Arithmetic and Logical Instruction**

**Assignment 1:**

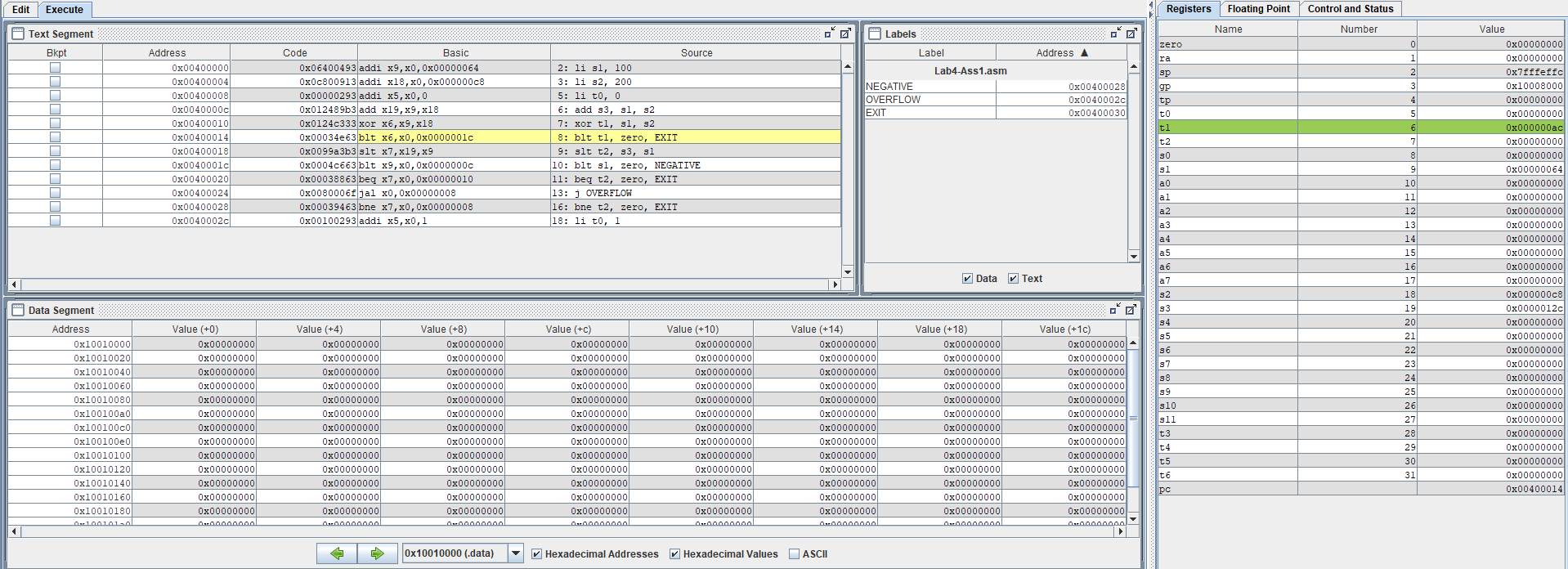
Case 1: both s1 and s2 are positive.

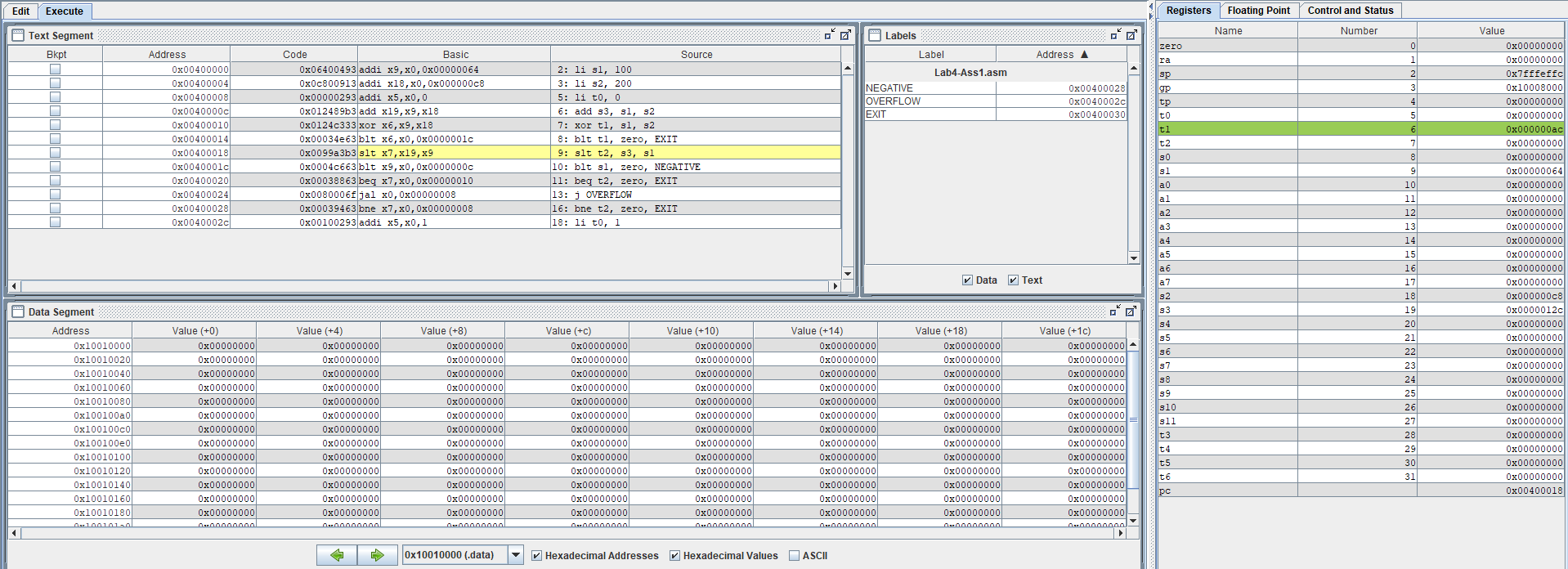
**A screenshot of a computer code

Description automatically generated**

**A screenshot of a computer

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**A screenshot of a computer

Description automatically generated**

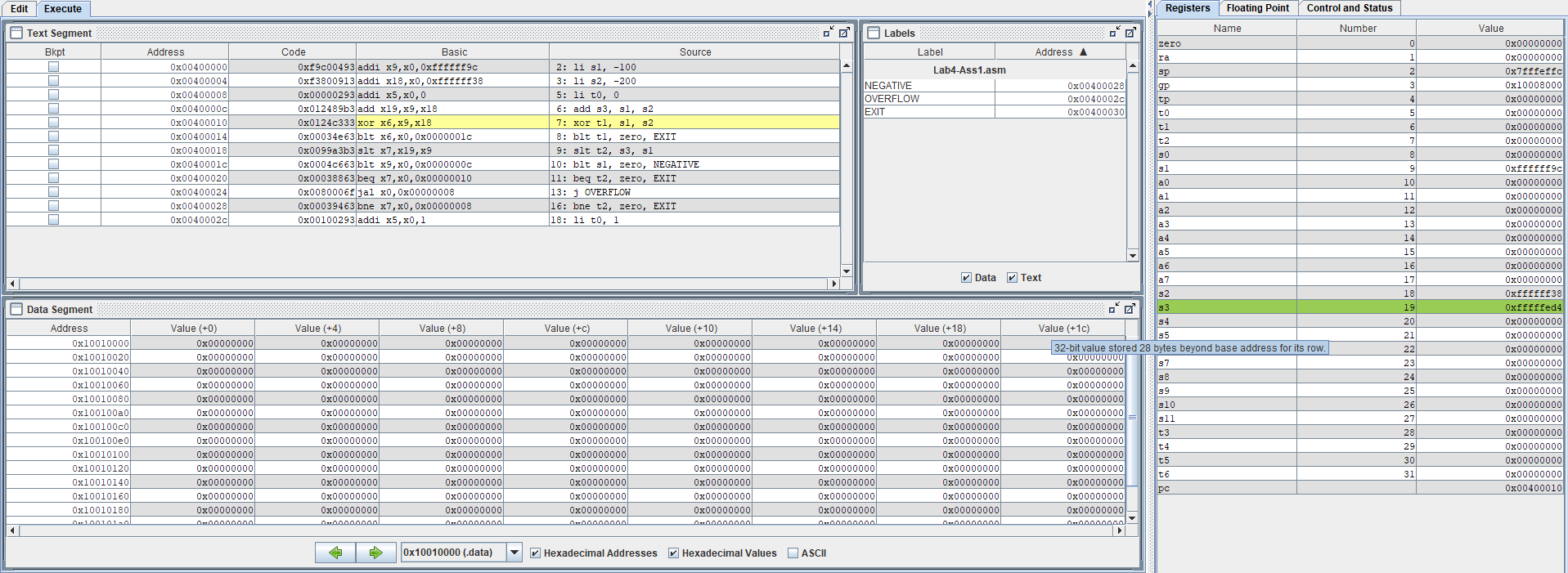
**A screenshot of a computer

Description automatically generated**

Case 2: both s1 and s2 are negative.

A screenshot of a computer

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A screenshot of a computer

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Case 3: s1 assign to max 32bit value, and s2 = 1.

A screenshot of a computer code

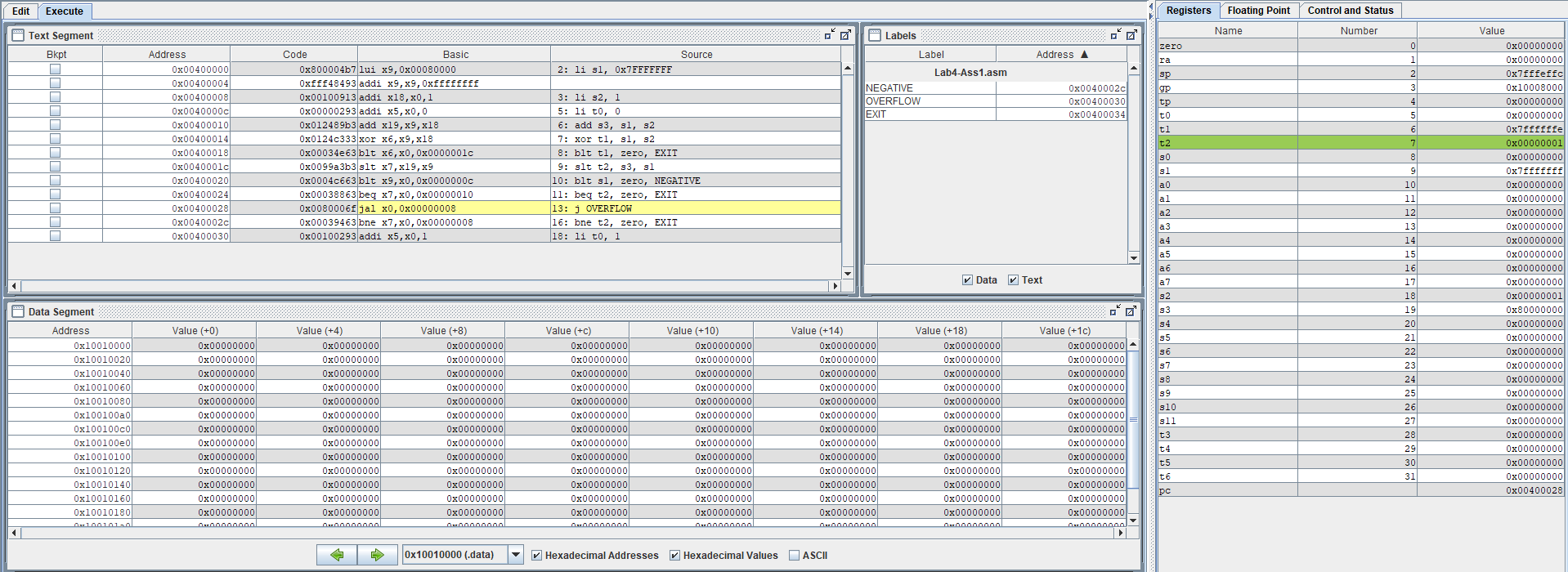
Description automatically generated

A screenshot of a computer

Description automatically generated

A screenshot of a computer

Description automatically generated



A screenshot of a computer

Description automatically generated

**Assginment 2:**

**A screenshot of a computer code

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**Assignment 3:**

A,

A screen shot of a computer code

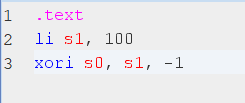
Description automatically generated

B,

A close-up of a number

Description automatically generated

C,

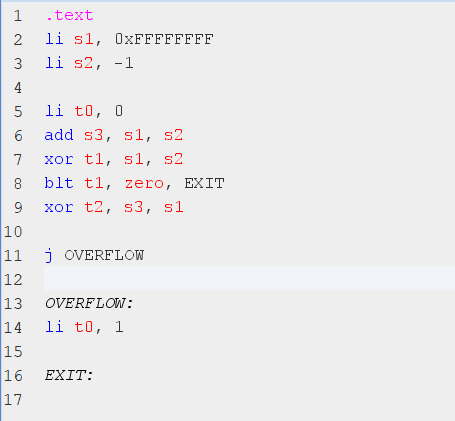


D,

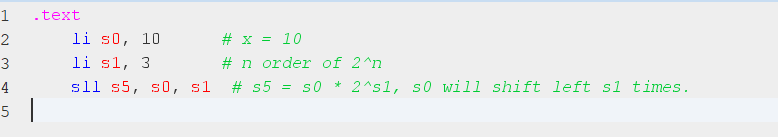
A screenshot of a computer

Description automatically generated

**Assignment 4:**

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**Assignment 5:**

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